Appl. No. 10/034,462
Amdt. dated May 26, 2005
Reply to Office action of March 8, 2005

<u>**Amendments to the Specification:**</u>

  Please replace paragraph [0001] with the following amended paragraph:

[0001] This application relates to the following commonly assigned co-pending applications entitled:

    Method And Apparatus For Efficiently Implementing Trace And/Or Logic Analysis Mechanisms On A Processor Chip, <u>Serial No. 10/034,717, filed December 28, 2001</u>(~~Invention Disclosure P01-3848~~); An Efficient Word Recognizer For A Logic Analyzer, <u>Serial No. 10/034,227, filed December 28, 2001</u>(~~Invention Disclosure P01-3850~~); <u>and</u> Method And Apparatus For Implementing Loop Compression In A Program Counter Trace<u>, Serial No. 10/034,506, filed December 28, 2001</u>~~(Invention Disclosure P01-3852); and Method And Apparatus For Implementing Loop Compression In A Program Counter Trace, (Invention Disclosure P01-3852).~~

Each of these co-pending applications is expressly incorporated by reference herein.